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Relevance scale ☐ ☐ ☐ ☐ ☐**1 An SBus monitor board**

H. A. Xie, K. E. Forward, K. M. Adams, D. Leask

February 1995 **Proceedings of the 1995 ACM third international symposium on Field-programmable gate arrays**Full text available:  pdf(68.64 KB)Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

During the development of computer peripherals which interface to the processor via the system bus it is often necessary to acquire the signals on the bus at the hardware level. It is difficult to attach general-purpose logic analysers and in-circuit emulators to a multiple pin bus connector and hence it is not practical to catch all the bus data required to ensure that such signals are in accordance with the bus specification. Hence a given connector specific bus monitor board is a necessity ...

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Evolvable Hardware, 1999. Proceedings of the First NASA/DoD Workshop on , 21 July 1999

Pages:12 - 17

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Field-Programmable Custom Computing Machines, 2000 IEEE Symposium on , 19 April 2000

Pages:319 - 320

[\[Abstract\]](#)   [\[PDF Full-Text \(180 KB\)\]](#)   IEEE CNF2 **High performance DES encryption in Virtex™ FPGAs using JBits™***Patterson, C.;*

Field-Programmable Custom Computing Machines, 2000 IEEE Symposium on , 19 April 2000

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| S6  | 3    | 717/124 and (programmable or reconfigurable or re-configurable or fpga) and (test\$3 or debug\$4 or simulat\$3) and ( plurality near5 hardware or hard-ware)                           | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2004/12/02 16:23 |
| S7  | 2    | debug\$4 near3 (embedded adj program) and (programmable or reconfiguarble or re-configurable or fpga or eeprom or eprom)   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2004/12/02 16:26 |
| S8  | 5    | debug\$4 near3 (hardware adj configuration) and (programmable or reconfiguarble or re-configurable or fpga or eeprom or eprom)   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2004/12/02 16:29 |
| S9  | 6    | ("6226776" "6009256" "6219628").pn.  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2004/12/02 16:29 |
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| S11 | 33   | ("5493507").URPN.  | USPAT   | OR | OFF | 2004/12/02 16:40 |
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| S13 | 19   | ("6052524").URPN.  | USPAT   | OR | OFF | 2004/12/02 17:15 |
| S14 | 6934 | (reconfigurable or reprogrammable or re-programmable or eeprom or eprom or fpga) same sensor   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON  | 2004/12/06 07:37 |
| S15 | 833  | (reconfigurable or reprogrammable or re-programmable or eeprom or eprom or fpga) same sensor same (test\$3 or debug\$4 or verify or verification or analysis or analyz\$5)             | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON  | 2004/12/06 07:50 |
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